

Alexander Lash

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EXPERIENCE

Software Engineer III, Google; New York, NY and Kirkland, WA – 2013-Present

Founding member of the Frameworks Apple Team (née iOS Developer Infrastructure); responsible for various domains including Swift language tooling, iOS releasing/provisioning, iOS internals, and iOS security and identity concerns. Previous projects include Google-wide iOS frameworks projects and the Google Sheets application.

Software Development Engineer II, Amazon; Seattle, WA – 2011-2013

Member of the Merchant Technology team; completely redesigned merchant middleware platforms while maintaining them. Created various operations visualizers and tools that brought the average on-call load (tickets, pages) down by 90%. Joined the Trade Books team in 2013; developed novel interfaces for book recommendation and discovery.

Software Development Engineer II, Microsoft; Redmond, WA – 2007-2011

Member of the Identity and Access team; designed and implemented a majority of the "cmdlets" for Active Directory PowerShell. Designed and prototyped a new classifier for Active Directory diagnostics capable of significant and fully automated root cause analysis of operational errors. Designed and implemented internal tools that fully automated build-deploy-test of projects freeing up 2-3 hours per developer per week.

Intern, 3Com; Marlborough, MA – 2006-2007

Member of the Compliance and Verification team; responsible for a ground-up redesign moving from manual to automated verification. Implemented a set of processes and guidelines to continually reduce reliance on manual testing. Maintained legacy J2EE and Perl applications.

Graduate Intern Technical, Intel; Chandler, AZ – 2006

Member of the Process Flow Integration team; conceived, designed, and implemented a scalable web-based document management and creation system. The system is used by roughly 10,000 users worldwide, supports XHTML/CSS and mobile XHTML web standards, and saves roughly 2 hours and 10 sheets of letter-size paper per user per week.

Undergraduate Intern Technical, Intel; Hudson, MA – 2005

Member of the Network Processing team; lead a group of interns in developing a 5gbps XML router, capable of handling both XPATH navigation and schema validation, on an experimental 64-core embedded platform. Revised several QA processes and procedures, reducing redundant testing overhead by 75%.

EDUCATION

Worcester Polytechnic Institute – Bachelor of Science, Computer Science, 2006

Worcester Polytechnic Institute – Master of Science, Computer Science, ongoing

SELECTED TALKS

TechEd, Orlando, 2008; Advanced Topics in Hyper-V Scripting (reprised four times by request.)
DefCon 16, Las Vegas, 2008; Taking Back Your Cellphone